

Summary of VHDL course

1. *LUT*
2. *LC*
3. *SLICE*
4. *CLB*
5. *FPGA*
6. *CPLD*
7. *COMPANY'S*
8. *VHDL*
9. *RTL Schematic*
10. *Technology Schematic*
11. *Synthesize*
12. *Implement*
 - A. Translate
 - B. Map
 - C. Place & Route
13. *Programming*
14. *Simulation*
15. *Test Bench*
16. *Core*
17. *DSP*
18. *Microcontroller*
- 19.